

FIG. 2

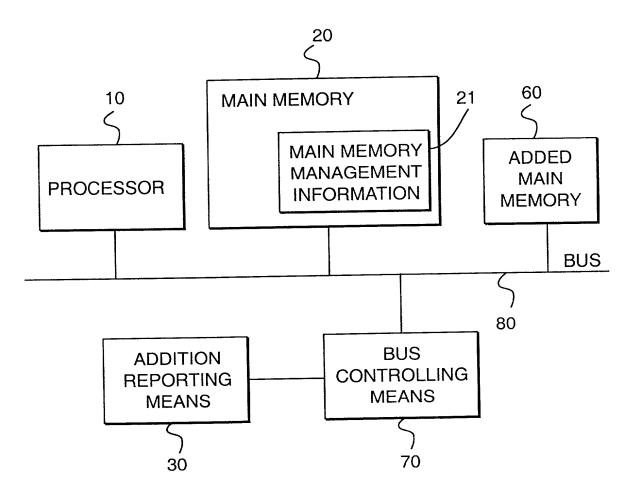
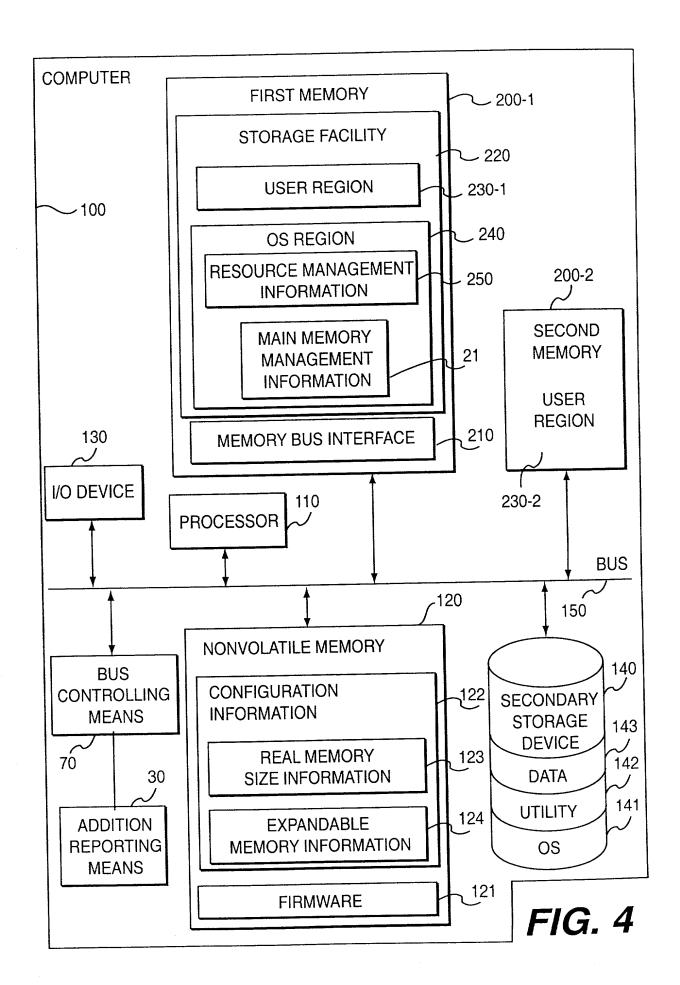
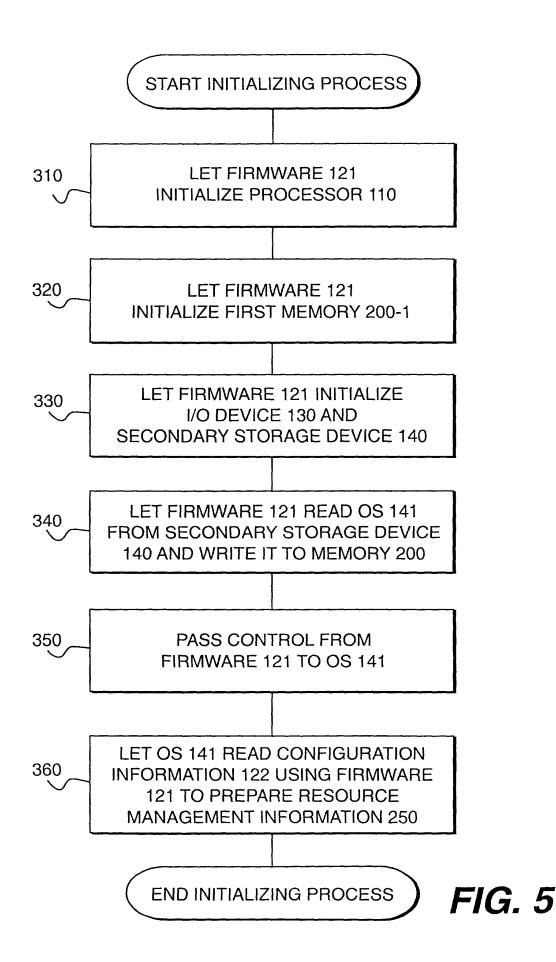


FIG. 3





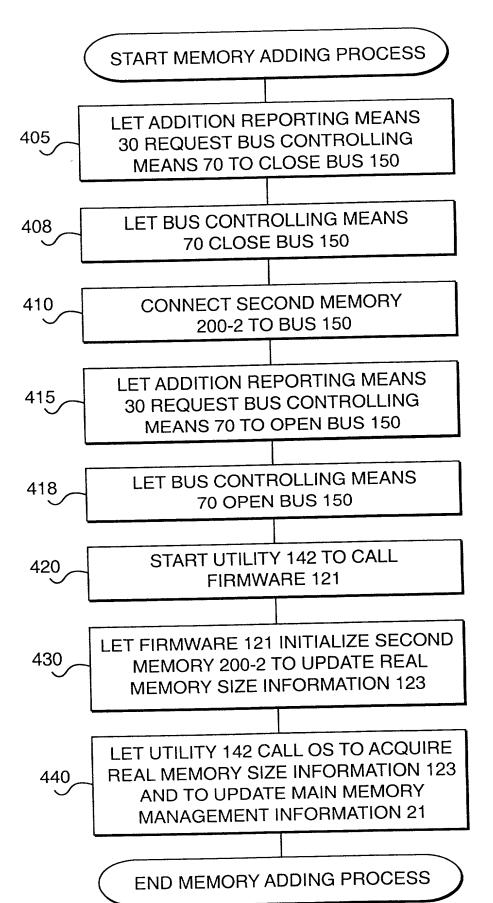


FIG. 6

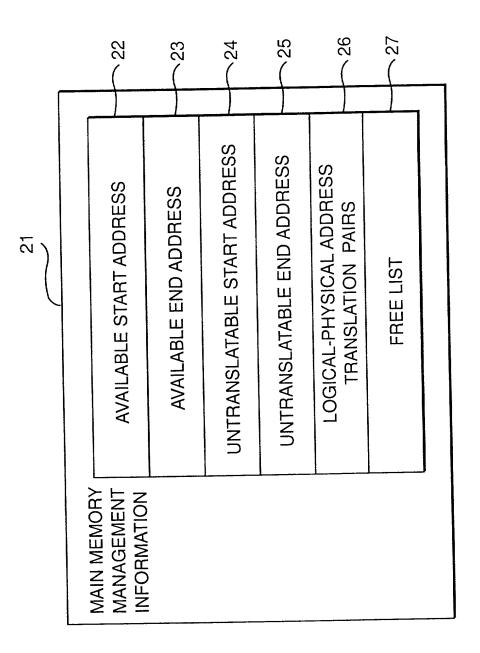
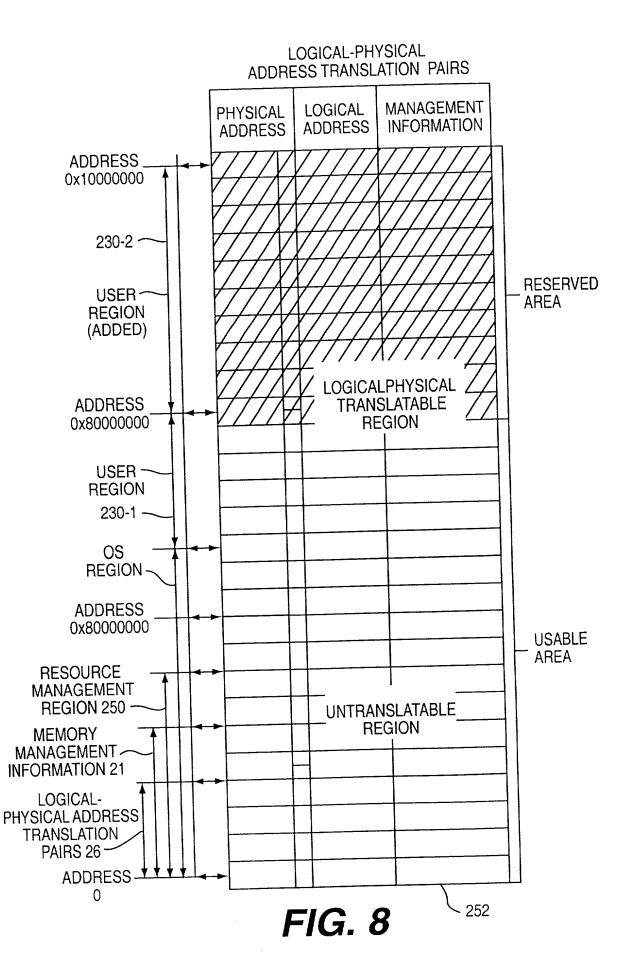


FIG. 7



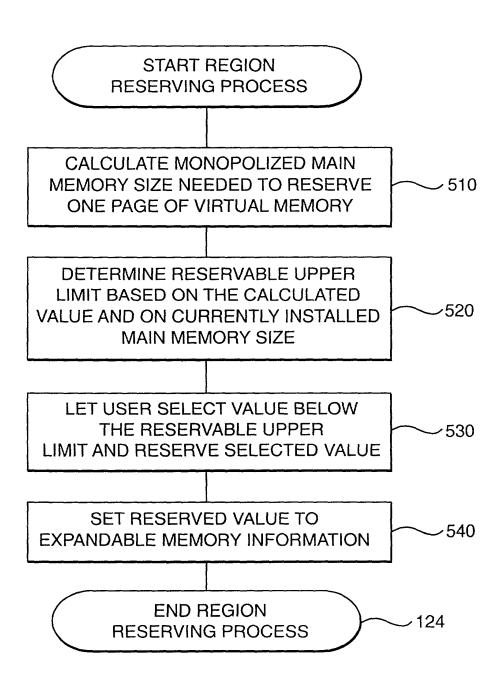


FIG. 9

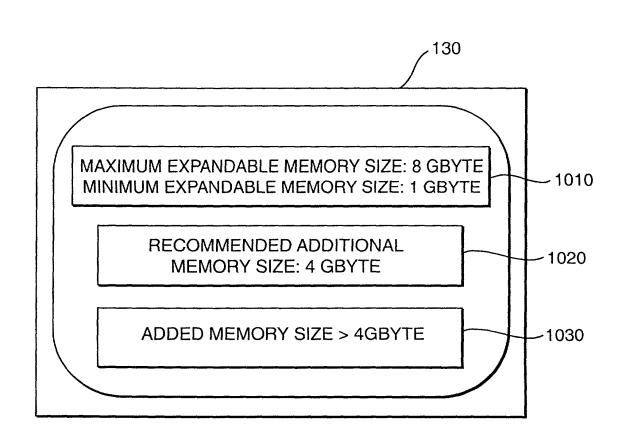


FIG. 10

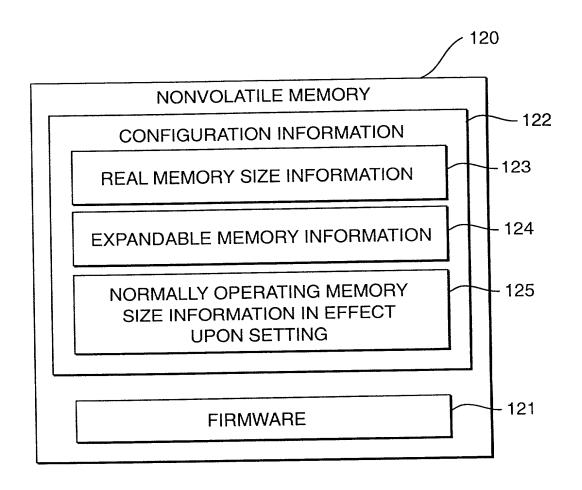


FIG. 11

moduli v

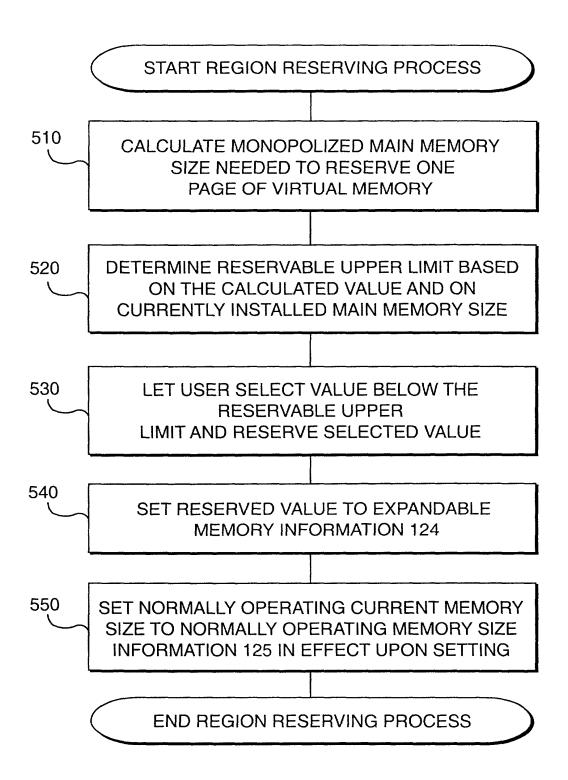


FIG. 12

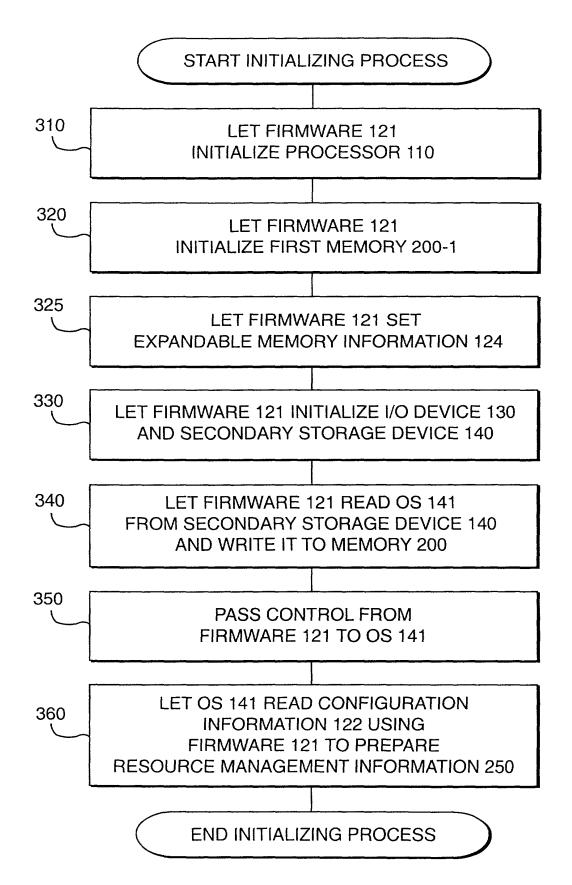
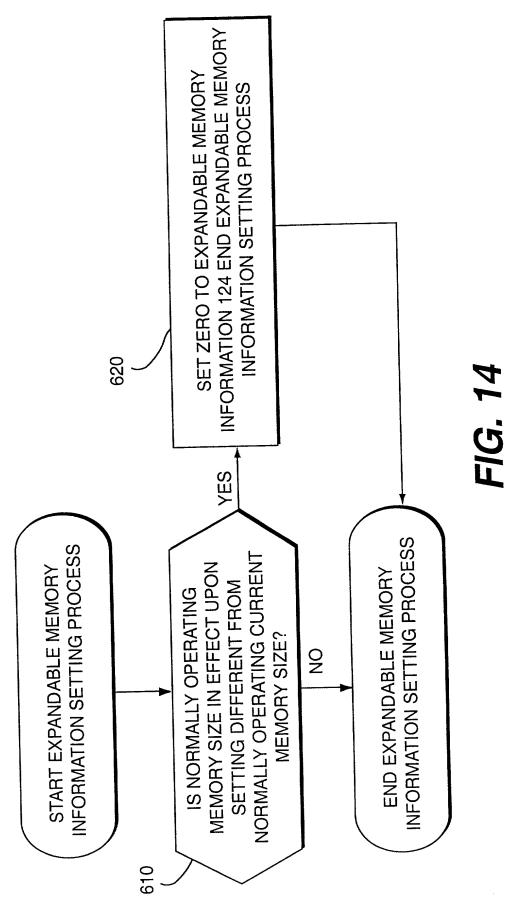


FIG. 13



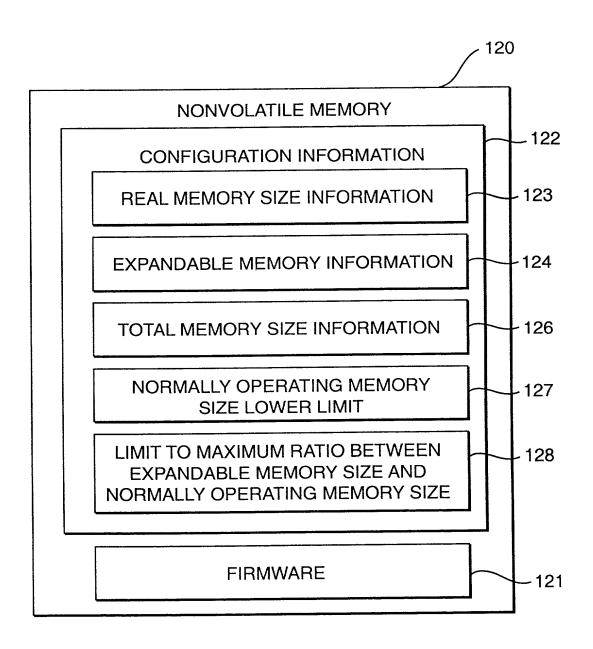


FIG. 15

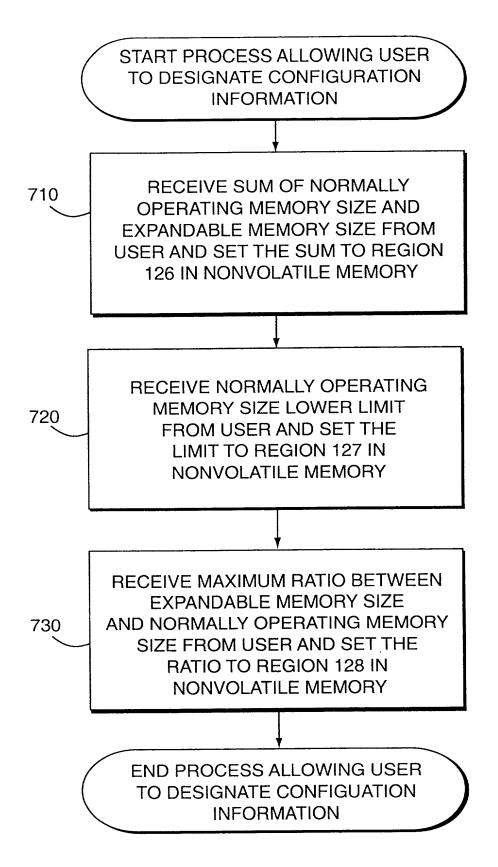


FIG. 16

